

SYSTEM ARCHITECTURES FOR 40GBE SERIAL



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System Vendor Requirements for 40G

- Systems vendors and end users would like to continue doing what they are doing – using SFP+ and duplex fiber for most applications
- While 40GbE QSFP+ has seen large deployments, shifting to 40GbE SFP40 will take 40GbE into even more environments at lower cost
- Switch-on-a-chip designs are expected to be the lowest cost solution and should be supported

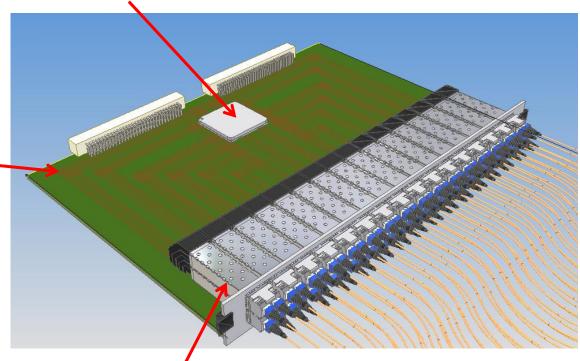


Switch on a Chip Design

Perfectly Matched QSFP+ to ASIC Port Count at 128X10GbE

128 Port ASIC at 10GbE To 1.28Tb/s of throughput

Long Traces to Modules at edges of boards – – some over 10"

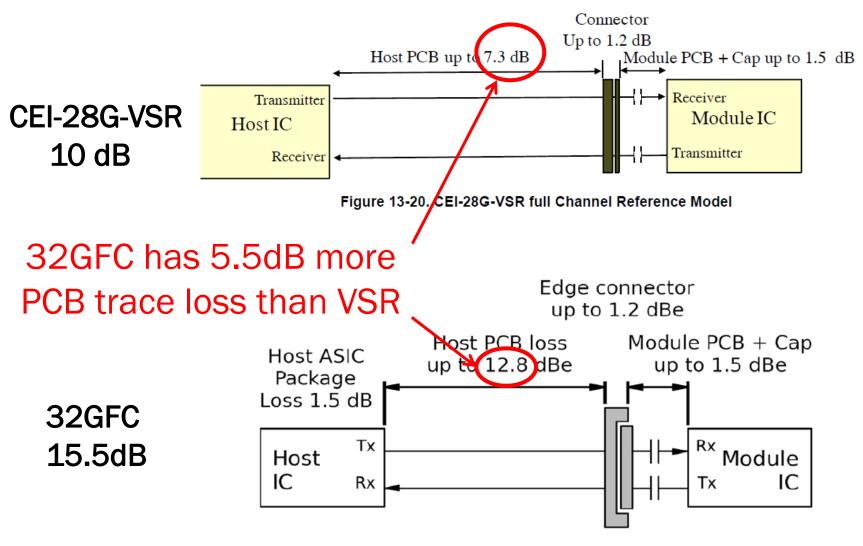


32 QSFP+ shown with 40GBASE-LR4

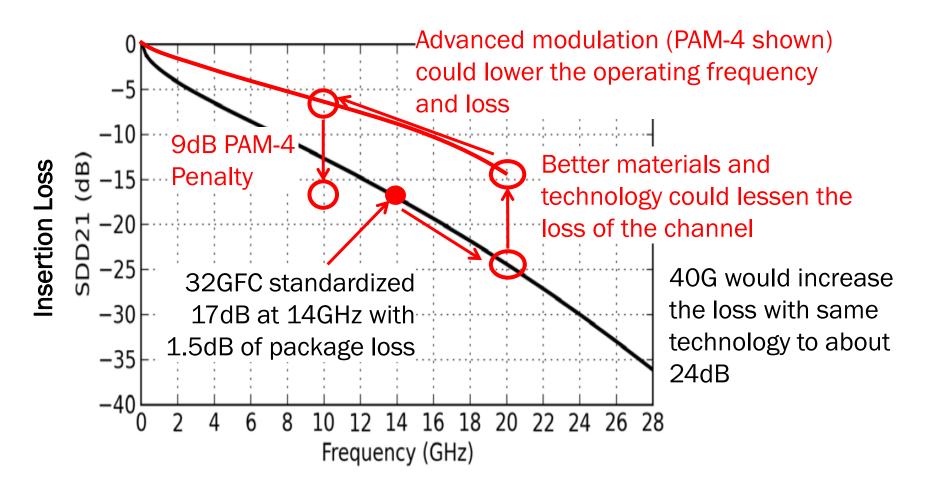


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CEI-28G-VSR vs 32GFC Insertion Loss

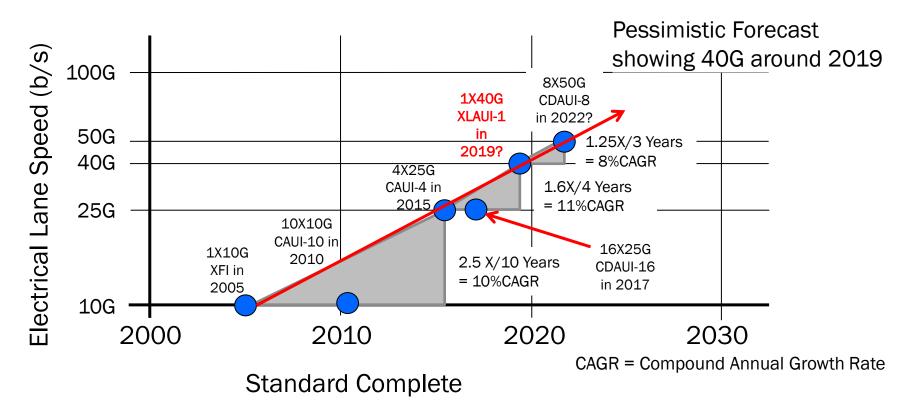


Insertion Loss from 32GFC to 40GbE Serial



40GbE Serial This Decade

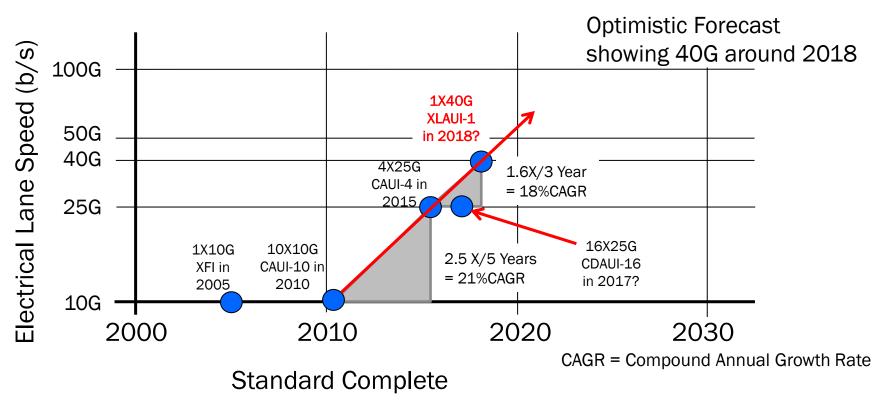
• Are you a pessimist?





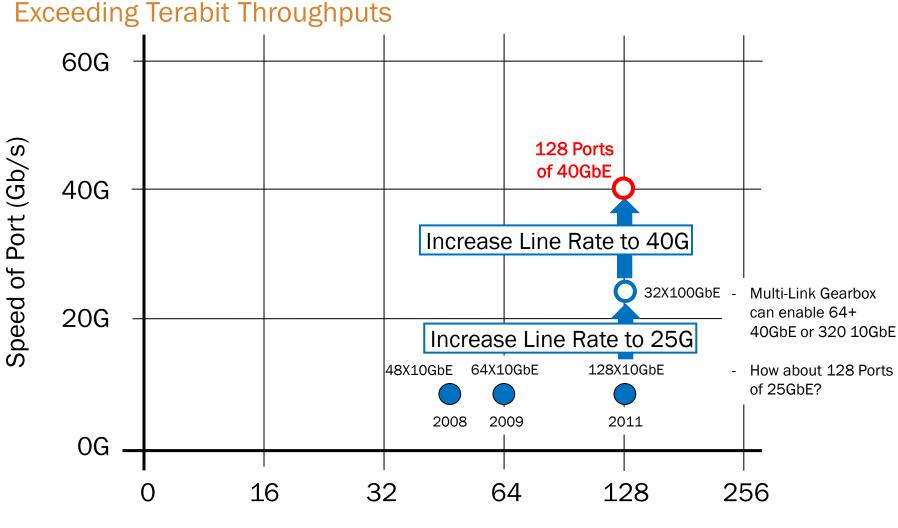
40GbE Serial This Decade

• Or an optimist?





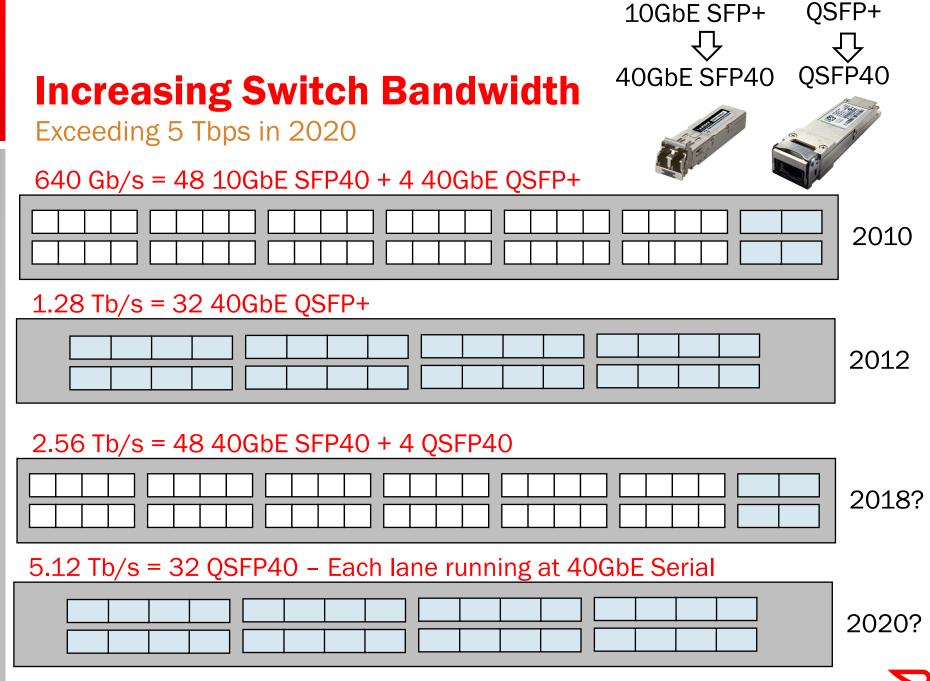
Ethernet Switch ASICs



Number of Lanes/Ports

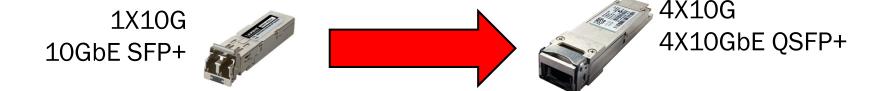


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The SFP and QSFP Dance

• 10GbE takes off with SFP+, but goes to QSFP+ for density



• 40GbE takes off with QSFP+, but could shift to SFP+



• 100GbE takes off with QSFP+, but could shift to SFP+

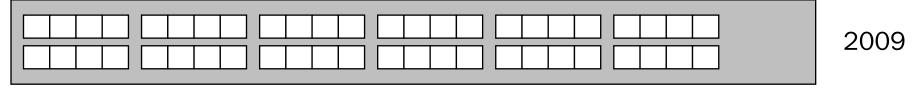




Summary of 480G of Throughput

Various ways to get to 480G

480 Gb/s = 48 10G SFP+, 24 Watts at 0.5W/ SR SFP+



480 Gb/s = 48 40G SFP40, 12 Watts at 1.0W/ SR SFP+



480G = 12 Embedded 40GbE ports, 10W/12 SR Ports





THANK YOU

